

APPLICATION FOR
UNITED STATES LETTERS PATENT
SPECIFICATION

TO ALL WHOM IT MAY CONCERN:

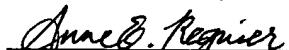
Be it known that **PAUL W. DENT**, a citizen of Great Britain, residing at 637 Eagle Point Road, Pittsboro, in the County of Wake and State of NORTH CAROLINA has invented a new and useful **METHOD AND APPARATUS FOR SUBTRACTING MULTIPLE RAYS OF MULTIPLE INTERFERING RECEIVED SIGNALS** of which the following is a specification.

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Anne E. Regnier

**METHOD AND APPARATUS FOR SUBTRACTING MULTIPLE RAYS
OF MULTIPLE INTERFERING RECEIVED SIGNALS**

CROSS REFERENCE

Sub
898,392

This application is a Continuation-In-Part to U.S. patent Application no. 09/082,722, filed May 21, 1998; U.S. Patent Application no. 08/989,392, filed July 22, 1997; and U.S. patent Application no. 09/340,907, filed June 28, 1999, each to applicant, which are the parent applications for this application and form part of this application in their entirety.

BACKGROUND OF THE INVENTION

This invention relates to decoding of received signals and, more particularly, to decoding quantized and unquantized wanted data symbols from received signal samples.

The code-division multiple access (CDMA) mobile communications system known as IS95 transmits from a base station to different mobile terminals in its coverage area (the downlink) using different 64-bit orthogonal codes. Each such code is of the same length (64 bits) and carries voice or data traffic of approximately the same data rate. Variable-rate orthogonal coding is not used in that system.

A wideband CDMA system known as 3G (third generation) has been standardized in a cooperation between the European Telecommunications Standards Institute (ETSI) and NTT DoCoMo of Japan, and specifies variable-rate orthogonal coding in which signals of higher bitrate can use orthogonal codes of a shorter length to increase the frequency of data symbol transmission

while still remaining orthogonal to lower bitrate transmissions using longer orthogonal codes and a lower frequency of data symbol transmission. The shortest orthogonal code presently specified is 4 chips long and the longest is 256 chips long.

Also in IS95, transmissions from mobile terminals to base stations use orthogonal codes to code different data symbol groups from the same mobile terminal but do not use orthogonal codes to distinguish between different mobile terminals. Different mobile terminal transmissions (the uplink) are distinguished by the use of different non-orthogonal, pseudorandom codes.

U.S. patents nos. 5,151,919 entitled "CDMA Subtractive Demodulation" and 5,218,619 also entitled "CDMA Subtractive Demodulation" to Applicant describe a CDMA system using orthogonal codes in the manner of the above-described IS95 uplink, in which different signals are successively decoded and subtracted in order from strongest to weakest in order to eliminate interference of the stronger signals upon the weaker signals. The '919 and '619 patents are hereby incorporated by reference herein.

In U.S. patent no. 5,572,552 entitled "Method and system for demodulation of downlink CDMA signals", Dent and Bottomley describe an optimum receiver for receiving CDMA signals at a mobile terminal transmitted from a cellular base station that subtracts non-orthogonal multipath rays in a multipath channel equalizer when own-base interference is dominant. Such a "channel inverse" equalizer is disclosed to be non-optimum in the presence of other-base interference or thermal noise and a hybrid equalizer method is described that lies between the conventional RAKE equalizer method and the channel inverse equalizer method. The '552 patent is hereby incorporated

by reference herein.

In U.S. patent Application no. 09/082,722, filed May 21, 1998, entitled "Partially Block-interleaved CDMA Coding and Decoding" to Applicant, methods for transmitting and receiving orthogonally-coded CDMA signals are described such that signals retain their orthogonality for most transmitted data symbols under multipath conditions. The above application is hereby incorporated by reference herein.

898,392

In U.S. Patent Application no. 08/989,392, filed July 22, 1997, entitled "Orthogonal Block-Spreading Codes for the Multipath Environment" further methods are described for compensating for multi-user interference on the residual symbols not retaining their mutual orthogonality by virtue of the Block-interleaving method. This application is hereby incorporated by reference herein.

Also, in U.S. patent Application no. 09/340,907, filed June 28, 1999, entitled "Multi-Carrier Orthogonal Coding", methods are disclosed for transmitting and receiving CDMA signals that are orthogonally coded over more than one frequency channel. This application is hereby incorporated by reference herein.

~~This application further extends the methods of decoding orthogonal signals described in the parent applications to compensate for multipath propagation even when block-orthogonal coding is not used.~~

SUMMARY OF THE INVENTION

A Code Division Multiple Access system using orthogonal codes of variable length to reduce interference between transmissions to different receivers of different underlying information rates transmits a sum of all signals to a receiver, which processes the received signal to separate a signal of highest bitrate from the remaining signals to substantially eliminate interference from the lower bitrate signals to the highest bitrate signal. The receiver may continue to process the residual signal after subtracting the separated highest bitrate signal to decode at least one of the lower bitrate signals thereby substantially eliminating interference from the highest bitrate signal to the lower bitrate signals.

BRIEF DESCRIPTION OF THE DRAWINGS

Figure 1 is a series of waveforms illustrating a wanted signal and a plurality of interfering signals, and energy amounts produced by said signals;

a Figure 2 is a block diagram of a receiver in accordance with the invention; and

Figure 3 illustrates superimposition of orthogonal spreading codes modulated with

15 different data symbols in multi-code transmission

DETAILED DESCRIPTION OF THE INVENTION

Figure 1 illustrates an exemplary signal transmitted by a wireless base station employing orthogonal CDMA codes. In this example, a single, high-power, high datarate signal

employing a short (2-chip) repeated code "x,y", where x and y are spreading code values, overlaps transmission with a multiplicity of other, orthogonal signals employing longer codes that always comprise pairs of spreading code values "x,-y" in succession. For example, x and y could be binary 1's giving a spreading code "11" and then x and -y in Boolean notation would be "10". Complex values "1,j" and "1,-j" are other examples of orthogonal chip pairs.

One implementation of the invention is described for the example where a high bit rate signal uses the spreading code "11" for successive transmitted symbols while lower bit rate signals use codes such as 10 01 01 10 , i.e. always a zero paired with a one in any chip pair corresponding to the chip pairs "11" of the high bit rate signal.

Assume the wanted symbols are S₀,S₁,S₂..... and are transmitted with spreading code 11, i.e. as S₀ S₀ S₁ S₁ S₂ S₂ S₃ S₃ etc. All of the other signals have spreading codes having 10 or 01 bit pairs following each other. If on the first chip of each pair the other signals with levels a,b,c... etc combine to give a+b-c-d ... etc, then on the second chip of each pair they have to give -(a+b-c-d....) i.e. the interfering waveform values due to the sum of all other signals are

15 W₀ -W₀ W₁ -W₁ W₂ -W₂ W₃ -W₃ etc

So the total transmitted signal waveform samples may be written as:-

S₀+W₀ S₀-W₀ S₁+W₁ S₁-W₁ S₂+W₂ S₂-W₂ ETC

These are received through some multipath channel comprising a shortest path or ray of earliest time-of-arrival with amplitude and phase described by the complex number C_0 , and other multipath rays or echos with successive delays of 1 chip, 2 chips etc of amplitude and phase described by complex numbers C_1, C_2 etc.

5 It is possible to include a filter in the receiver ahead of all other processing which has the effect of ensuring that earlier paths or "taps" after applying such a pre-filter contain the most energy, and coefficients $C_0, C_1, C_2 \dots$ now include the effect of the receive prefilter as well as the multipath propagation channel and any transmit filtering employed. The earlier paths communicate the latest symbols and the later paths are echoes of earlier symbols.

Received signal sample values $Z_0, Z_1, Z_2 \dots$ are thus described by:

$$C_0(S_0 + W_0) + C_1(S_{-1} - W_{-1}) + \text{earlier symbols} \dots = Z_0$$

$$C_0(S_0 - W_0) + C_1(S_0 + W_0) + \text{earlier symbols} \dots = Z_1$$

Assume that the earlier symbols such as S_{-1} and interfering waveform values W_{-1} have already been separated from each other in a previous iteration, and the desire is now to separate S_0 from W_0 .

15 Terms involving earlier values of S and W can thus be subtracted from both sides leaving modified values Z_0', Z_1' on the RHS, giving the 2x2 matrix equation:-

$$\begin{array}{cccc} C_0 & C_0 & S_0 & Z_0' \\ & x & & = \\ C_1+C_0 & C_1-C_0 & W_0 & Z_1' \end{array}$$

Such equations can be solved so long as the determinant of the matrix does not approach zero, and the determinant of the above coefficient matrix is clearly the same as the determinant of the matrix

$$\begin{matrix} \text{Co} & \text{Co} \end{matrix}$$

$$\begin{matrix} \text{Co} & -\text{Co} \end{matrix}$$

which is -2Co^2 and never zero or ill-conditioned, due to choosing the prefilter so that Co is as large as possible.

The equations can thus be solved for So and Wo, obtaining

$$So = (Zo' + Z1')/2Co - C1.Zo'/2Co^2$$

$$Wo = (Zo' - Z1')/2Co + C1.Zo'/2Co^2$$

and those values can be fed forward after quantizing So to a nearest symbol in the alphabet when resolving S1 and W1 from the next two signal samples.

One known method in which previously decoded symbols are fed forward to cancel intersymbol interference (ISI) when decoding future symbols is called Decision Feedback Equalization or DFE. Prior art DFE was concerned only with interference between symbols of a wanted symbol stream, and not with interference from unwanted signals. Moreover, in pure DFE according to the prior art all fed forward symbols were quantized or "decided" to nearest legal values in the symbol alphabet. In the above formulation however, it is seen that the values fed forward comprise a mixture

of quantized values like S_0 which represent cancellation of ISI from one wanted symbol to the next and unquantized values like W_0 which represent the sum of all other, unwanted signals.

Thus all other interferers emanating from the same base station transmitter are subtracted in one shot at the same time as the wanted signal is equalized for multipath propagation,
5 when practicing the above method.

In DFE methods, the multipath ray that principally contributes to deciding the value of a data symbol is the ray with coefficient C_0 , while the rays with coefficients $C_1, C_2 \dots$ are treated as unwanted interference and subtracted.

In the alternative Viterbi Maximum Likelihood Sequence Estimator (MLSE), all rays are regarded as providing useful clues to a data symbol's value. One explanation of MLSE is that all possible decodings for the current symbol are retained along with a metric for each, indicative of the error between the unquantized value of S_0 and each quantized value. Then the next symbol is decoded once for each of the retained assumptions being fed forward in turn, to obtain multiple decodings for the next symbol. Then, of all possible decodings for the next symbol S_1 giving a particular value of S_1 , that having the lowest cumulative metric is retained, along with the value of S_0 that was fed forward to obtain it. When this is repeated for each particular value of S_1 , a full set of possible retained S_1 values is obtained, each with a cumulative metric and the associated history of fed forward symbols. It is also possible to use MLSE to hypothesize "future" symbols, such as S_2 when decoding S_1 , as well as retaining all values of S_0 to feed forward. It is then possible to decode
20 a signal having passed through a channel for which C_0 is not the earliest significant ray, there being

an even earlier ray with coefficient $C_{,1}$ that multiplies the "future" symbol. When hypothesizing two symbols to decode another, the number of retained results corresponds to all combinations of the last two symbols, and therefore the number of metrics is also greater. The collection of hypothesized symbols with their associated path histories and cumulative metrics form the Viterbi "States". The 5 Viterbi MLSE algorithm is useful when no good receive prefilter can be found that makes C_0 the largest channel coefficient, and so long as the collection of states to deal with future symbol hypotheses does not become too numerous. When the MLSE algorithm is used with the current invention, there is not only a saved path history of decided symbols S_0, S_1, S_2, \dots associated with each state but also a saved history of the associated unquantized values W_0, W_1, W_2, \dots etc.

When the above-described DFE or Viterbi interference cancelling decoder has operated to decode the high bit rate symbol stream S_0, S_1, S_2, \dots etc it will also thus have separated out a stream of unquantized values W_0, W_1, W_2, \dots etc which represent the sum of all the other signals, also compensated for multipath distortion. It is possible to save and then further process these unquantized values to decode any of the other signals contained therein, for example by despreading a signal using the rest of its orthogonal code (it was already despread by the first factor of 2) and now it will be found that the other signals are perfectly orthogonal, as the multipath has been compensated. Examination of how the multipath on the other signals has been compensated above will however reveal that it is equivalent to the use of the "inverse channel" filter mentioned in the above-incorporated '552 patent, which is not the optimum filter when other transmitter interference 20 is significant. However, the saved samples W_0, W_1, W_2, \dots can be subjected to the inverse of the

"inverse channel" filter if desired, and then subjected to a different filter or indeed an iterative reapplication of the above procedure to decode a next lowest spreading factor signal from the others, and so forth. Thus one implementation of the invention can comprise successive decoding and subtraction of successively lower bit rate signals, the order of decoding in this method being in descending order of bit rate rather than in descending order of signal level, as in the incorporated references.

Figure 2 illustrates a receiver 10 to decode a highest bit rate CDMA signal while compensating for non-orthogonal multipath interference. The receiver 10 is adapted for use in a mobile communications system including a plurality of base stations ^{50, see Figure 4,} ^{52 and 54} and mobile terminals ^{56, 58 and 60}.
Particularly, the receiver 10 represents either the mobile terminal or the base station used in a mobile communications system. ⁵⁰ ⁶²

The present invention is described herein in the context of a mobile terminal. As used herein, the term "mobile terminal" may include a mobile communications radiotelephone with or without a multi-line display; a Personal Communications System (PCS) terminal that may combine a mobile communications radiotelephone with data processing, facsimile and data communications capabilities; a PDA that can include a radiotelephone, pager, Internet/intranet access, Web browser, organizer, calendar and/or a global positioning system (GPS) receiver; and a conventional laptop and/or palmtop receiver or other appliance that includes a radiotelephone transceiver. Mobile terminals may also be referred to as "pervasive computing" devices.

20 The receiver 10 includes an antenna 12 that receives radio signals which are then

filtered and amplified in a block 14 to select a desired frequency channel. The selected signals are analog-to-digital converted to representative digital samples in an A to D convertor 16 which can operate according to any conventional or known method. The digital samples are processed in an Initial Channel Estimator 18 to determine an initial channel estimate including the above-named 5 channel coefficients C_0, C_1, \dots etc. The initial channel estimates are so called because they are subsequently modified when the prefilter is calculated in a prefilter calculation block 20, and also because values based on initial channel estimates, which are usually made using only known symbols inserted into the signal by the transmitter, can be refined later by using unknown symbols as now-known symbols after they have been decoded, a process known as channel tracking.

The initial channel estimates are used to determine, in the prefilter calculation block 20, the coefficients of a prefilter 22 through which the digital signal samples from the A to D converter 16 are passed in order to change the initial channel to a modified channel in which most of the wanted signal energy appears in an earliest ray and a minimum amount of wanted signal energy appears in rays earlier than that, i.e. minimum dependence on "future" symbols, only dependence on 15 "past" symbols. The new channel estimates after the signal has been filtered by the prefilter 22 are also calculated in the prefilter calculation block 20 and passed with the prefiltered signal samples to *control represented by a* a subtract block 24. If the A to D convertor 16 oversamples the signal at more than one sample per symbol, prefiltering can also comprise selecting or computing only one sample per symbol as the prefilter output, the selected or computed output sample having the above-described property of *may be* maximum energy in the earliest ray, i.e. the modified channel coefficient C_0 is preferably larger than *may be*

1 C1, C2 etc and coefficients $C_{i,1}$ and earlier ^{may be} are preferably smaller.

At the subtract block 24, the above equations are used to subtract the influence of an

earlier-determined symbol S_i and an earlier-determined interference waveform value W_i . In a
^{Quantizer represented by a} quantize block 26, the signal, with the influence of earlier symbols and interference now subtracted

5 is separated into a quantized symbol $S(i+1)$ and a new waveform point $W(i+1)$. The value of $W(i+1)$ obtained by the above solution of two simultaneous equations for the unquantized symbol value S and W may optionally be modified by plugging back into the equations the quantized value of $S(i+1)$ to obtain a new value of $W(i+1)$ that now depends to which symbol value $S(i+1)$ was quantized. When a Viterbi (MSLE) algorithm is used to retain multiple possible quantizations of $S(i+1)$, there will thus be corresponding multiple values of $W(i+1)$. These multiple W -values may be obtained by plugging the quantized value of S into the two equations and solving them now in a least squares sense for the single remaining unknown W .

The determined $S(i+1)$ and $W(i+1)$ values are then fed back (decision feedback) via a delay block 28 to the subtract block 24 to subtract their influence on the next two signal samples 15 to be decoded, and the stream $S_i, S(i+1)$ is output. The unquantized values of S may alternatively be output as "soft information" to an error correction decoder, such as a convolutional decoder. If the symbols S are binary (BPSK) bits or quaternary (QPSK) bit pairs, relating the soft output values to bit likelihood values required by the error-correction decoder is trivial. If higher order constellations such as 16QAM (quadrature amplitude modulation) or 8-PSK (phase shift keying) are 20 used, then these M-ary symbol soft values may have to be converted to bitwise soft information,

which however can be done according to the method of U.S. patent application no., filed Feb. 8, 2000, entitled "Methods and Systems for Decoding Symbols by Combining Matched-Filtered Samples with Hard Symbol Decisions" which is hereby incorporated by reference herein.

When more than one previously decoded symbol value and interference waveform affects the next

5 two signal samples, the symbol and waveform values are passed through an FIR filter comprising the channel coefficients of the delayed rays C1,C2,C3 etc to determine the values that shall be subtracted, thus collecting together their contributions that are simply denoted in the above equations by " + earlier symbols".

When a Viterbi MLSE algorithm is used, such an FIR filtering and interference subtraction may be performed "per state", using the symbol and interference waveform history associated with each state, also known as "per survivor processing".

The above description was simplified to assist understanding by considering only a high bit rate signal using a 2-chip spreading code while all other signals used longer orthogonal codes. If another signal had also used an orthogonal 2-chip code, then there could only be one other interfering signal as there are only two, 2-chip orthogonal codes. The method may then advantageously be converted to a joint demodulation method to demodulate both signals at the same time, by quantizing both S and W values, the quantized W-values then representing the other signal's symbols. Joint demodulation of two overlapping signals is known in the art, as is joint demodulation of multiple overlapping signals. Where the current invention differs however, is that only the sum of

15 multiple interfering signals need be determined as the waveform samples W, and not their individual

20

symbols. Thus there is a considerable reduction in complexity when practicing this invention for interference cancellation.

The invention may however be extended to include the case where the high bit rate signal uses one or more orthogonal codes longer than 2 bits. For example, the wanted signal may use 5 the 4-chip code 1010 repeated, while other signals use codes composed of the groups 1111, 1100 and 1001. The wanted signal may also comprise a symbol stream using one of the above four codes plus a symbol stream using another of the codes, both of which are to be separated from each other and from the other interferers. Before describing this extension of the invention however, another generalization is discussed. A high bit rate signal using the 2-chip spreading code 11 (repeated) will be found to have a spectrum largely confined to the central part of the frequency channel, while other signals using codes composed of repeated 10 or 01 pairs have spectra largely confined to the outer parts of the channel, or vice-versa. Alternatively, a signal using complex 2-chip spreading code $(1,j)$ as opposed to the orthogonal $(1,-j)$ would be found to be confined to the upper as opposed to the lower part of the frequency channel. To avoid particular signals being restricted to only certain parts 15 of the spectrum, resembling frequency-division multiple access (FDMA), assignment of codes to signals can be permuted from symbol to symbol thereby achieving "spectral hopping" which causes each signal to cover the whole channel spectrum in the mean. Spectral hopping is different from "frequency hopping" in that the latter comprises hopping between single sub-bands or channels, while the former comprises hopping between different spectral shapes that are not necessarily restricted to 20 a single sub-band. Another method of ensuring that every signal covers the entire spectrum is to

apply a pseudorandom sequence of complex rotations to each chip, the same rotation being applied to all signals alike so as not to disturb their mutual orthogonality. When decoding the signal as above, the known phase rotations applied to the signals must be accounted for when solving the above equations and when subtracting the influence of earlier-decoded values. This may be done by 5 including the pseudorandom rotation with the channel coefficients used at each iteration, as a phase rotation applied at the transmitter is effectively part of the channel phase through which a symbol propagates.

Received signal sample values $Z_0, Z_1, Z_2\dots$ are then described by:

$$A_1.C_0(S_0+W_0) + A_0.C_1(S_{-1} - W_{-1}) + \text{earlier symbols} \dots = Z_0$$

$$A_2.C_0(S_0-W_0) + A_1.C_1(S_0+W_0) + \text{earlier symbols} \dots = Z_1$$

As before, terms involving earlier values of S and W can be subtracted from both sides leaving modified values Z_0', Z_1' on the RHS, giving the 2×2 matrix equation:-

$$\begin{array}{cccc} A_1.C_0 & A_1.C_0 & S_0 & Z_0' \\ & x & & = \\ 15 & A_2.C_0+A_1.C_1 & A_1.C_1-A_2.C_0 & W_0 & Z_1' \end{array}$$

where $A_0, A_1, A_2\dots$ etc is the sequence of complex chip rotations, i.e. the A -values are of amplitude unity and pseudorandom phase.

The first equation can be "derotated" by $A1^*$ and the second by $A2^*$ leaving:

$$\begin{array}{cccc}
 Co & Co & So & A1^*.Z0' \\
 \times & & = & \\
 Co + A2^*A1.C & -Co + A2^*A1.C1 & W0' & A2^*.Z1'
 \end{array}$$

5 This is the same equation as before but with $Z0'$, $Z1'$ and $C1$ replaced by phase-rotated versions of those quantities due to the A -factors. Consequently, the equations can be solved in the same way as previously described by use of the known complex spreading sequence given by the A -values to modify the phase angles of the $C1$, $Z0'$ and $Z1'$ values, giving

$$So = (A1^*.Zo' + A2^*.Z1')/2Co - A2^*A1.C1.A1^*.Zo'/2Co^2$$

$$Wo = (A1^*.Zo' - A2^*.Z1')/2Co + A2^*A1.C1.A1^*.Zo'/2Co^2$$

or

$$So = (A1^*.Zo' + A2^*.Z1')/2Co - A2^*.C1.Zo'/2Co^2$$

$$Wo = (A1^*.Zo' - A2^*.Z1')/2Co + A2^*.C1.Zo'/2Co^2$$

15 The first term of the solution is just the "despread value" which is produced by correlating the received signal samples with the conjugate of the complex spreading sequence while the second term represents subtraction of multipath interference. It is acceptable to subtract multipath interference so long as the prefilter has modified the channel to ensure that Co is the dominant channel coefficient

containing the majority of the wanted signal energy.

One type of prefilter that ensures that all the wanted signal energy appears in a single channel coefficient is the time-reverse conjugate filter. This is an FIR filter with coefficients $C_2^*, C_1^*, Co, C_{-1}^*, C_{-2}^*$ when the channel coefficients are $C_2, C_1, Co, C_{-1}, C_{-2}$.

5 Convolving the channel with the time-reversed conjugate channel yields a modified channel coefficients:

$$C_2^* C_2$$

$$C_2^* C_{-1} + C_1^* C_{-2}$$

$$C_2^* Co + C_1^* C_{-1} + Co^* C_{-2}$$

$$C_2^* C_1 + C_1^* Co + Co^* C_{-1} + C_{-1}^* C_{-2}$$

$$|C_2|^2 + |C_1|^2 + |Co|^2 + |C_{-1}| + |C_{-2}|^2$$

$$C_1^* C_2 + Co^* C_1 + C_{-1}^* Co + C_{-2}^* C_{-1}$$

$$Co^* C_2 + C_{-1}^* C_1 + C_{-2}^* Co$$

$$C_{-1}^* C_2 + C_{-2}^* C_1$$

$$15 C_{-2}^* C_2$$

It can be seen that the modified channel has a Hermitian symmetry about a center coefficient which is just the sum of the powers in all the multipath rays. If the effect of all the other rays could simply be subtracted therefore, and data decoded using only the center term, the performance would be as

good as the total signal power in all rays, which is the best possible performance.

It has already been described how rays of positive delay can be subtracted by using already decoded symbols and interference waveform values. However, the above Hermitian-symmetric channel has rays of negative delay relative to the main ray, which require as yet undecoded symbols to subtract their effect. The Viterbi algorithm can be used to postulate all possible combinations of future symbols, so long as these are not too numerous, and to decode the present symbol for each postulate. When the next symbol is decoded, the previous decodings are pruned to only those that were made for that value of the next symbol, and so forth. The number of states needed for the Viterbi approach may be reasonable for binary symbols, but explodes if the data symbols are from a larger alphabet such as quaternary, 8-PSK, 16QAM or the like. Thus an alternative approach is needed when the symbol alphabet is large.

Figure 1 illustrates a wanted signal 50 with a 2:1 spreading, which consumes the code space of two 4:1 spread signals, plus a first interfering signal 52 spread 4:1, now occupying the code space of three out of four 4:1 spread signals, and the remaining 1/4 of the 4:1 code space is occupied by second and third 8:1 spread interfering signals 54 and 56.

An alternative however might be a pair of 4:1 spread wanted signals, each carrying half the data symbol rate, which occupies an equivalent amount of code space to a 2:1 spread signal. Yet another alternative would be eight 16:1 spread signals, each carrying 1/8th the data symbol rate, with the other half of the code space occupied by unspecified but orthogonal interferers. In this latter case, each wanted symbol is eight times as long in time duration as in the 2:1 spreading example. As

a result, many more of the delayed rays of 1,2,3,4 etc chips delay comprise multipath interference that is largely within the duration of the same group of wanted data symbols. This can be exploited to alleviate the need for a complex Viterbi algorithm for compensating the rays of negative delay.

Figure 3 illustrates an exemplary case of such multi-code transmission. Each square of figure 3 represents the superimposition of N , N -chip orthogonal codes, each modulated with a different data symbol. Thus each of the N different symbols is constant for the duration of one square, but is modulated by the N chips of an orthogonal code with a chip duration of $1/N$ th of a square. A square containing N such symbols of information is received through a multipath channel, ^{and may be} \wedge prefiltered as described above so that there is a dominant ray or path with channel coefficient C_0 . Other delayed rays have channel coefficients C_1, C_2 etc while rays of shorter delay than the main ray have channel coefficients C_{-1}, C_{-2} etc. The symbols in successive squares are the sets labeled S_{i-1}, S_i, S_{i+1} etc. To decode the symbols in a square, the signal is sampled N times yielding complex samples $Z_0, Z_1, \dots, Z(N-1)$. The sample position for Z_0 is indicated by the heavy vertical dashed line, and the squares through which it passes indicate which rays and symbols contribute to the sample value. It can be seen that Z_0 depends on the current symbol set S_i through the main channel C_0 and on the current symbol set advanced by 1, 2 and 3 chips through the channel coefficients C_{-1}, C_{-2}, C_{-3} as well as the previous symbol set S_{i-1} delayed by one, two and three chips and weighted by the channel coefficients C_1, C_2, C_3 respectively.

The next sample, Z_1 , will depend on the current symbol set S_i through channels C_1, C_0, C_{-1} , and C_{-2} , the previous symbol S_{i-1} through channel coefficients C_2 and C_3 , and the future

symbol set S_{i+1} through channel coefficient $C_{.3}$. Successive samples will depend less on past symbol set S_{i-1} and more on future symbol set S_{i+1} . The above is entirely expressed by the matrix equations

$$Z_i = A \cdot S_{i-1} + B \cdot S_i + C \cdot S_{i+1}$$

where Z_i is the sample vector ($Z_0, \dots, Z(N-1)$) for square "i", the S -values are vectors of the corresponding symbol sets, and A, B and C are square matrices, the elements of which are combinations of the channel coefficients with signs given by the orthogonal code values. This equation can be rearranged to give the solution for S_i as

$$S_i = B^{-1} Z_i - B^{-1} A \cdot S_{i-1} - B^{-1} C \cdot S_{i+1}$$

Assuming the past symbol set has been decoded, but the future symbol set has not, an approximation for S_i can be obtained by setting the future symbol set S_{i+1} to zero. The symbols of S_i may then be quantized to the nearest values in the alphabet for feeding forward when S_{i+1} is decoded. The unquantized values may be converted to bitwise soft information for feeding to an error-correction decoder. If desired, after a similar approximation has been obtained for S_{i+1} , it can be fed back to improve the decoding of S_i . Alternatively, the similar expressions for future symbols, can be 15 back-substituted algebraically to obtain an expression for S_i of the form

$$S_i = U_0 \cdot Z_i + U_1 \cdot Z_{i+1} + U_2 \cdot Z_{i+2} \dots - V \cdot S_{i-1}$$

where the successive matrices U_1 , U_2 are hopefully diminishing to zero after a few terms. These matrices are constant so long as the channel coefficients and the orthogonal codes are constant, and may be precomputed in this case.

5 When the orthogonal codes are Fourier sequences, each "square" of figure 3 is transmitting its symbol set using different sub-carriers of an Orthogonal Frequency Division Multiplex (OFDM) system. When the orthogonal codes are Walsh codes however, which have been termed "sequencies" in analogy with the "frequencies" which characterize Fourier sequences, the modulation may be termed Orthogonal Sequency Division Multiplex (OSDM). As the number of chips in the code or square is increased along with the corresponding size of the symbol sets S_i , i.e. as the order of the OSDM is increased, the channel delay spread is more and more confined to the limits of one square in duration, with Inter-square Interference occurring only for the chips at the edges of the square. Moreover, only the chips at the trailing edge are affected by future symbol sets, which have to be assumed to be zero to obtain the simple approximate solution derived above. Therefore the approximation will improve with increasing order of OSDM and for an expected maximum amount of delay spread in a given application, such as wireless data networks, there will be an appropriate order of OSDM that permits the approximate solution to perform adequately.

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Other aspects of a complete receiver such as channel tracking may be derived from known prior art methods. For example, an initial estimate of the channel coefficients C_i may be made

using known symbols transmitted by the transmitter, and then refined after decoding data symbol sets by using them as extensions of the known symbols. One or more symbols of a symbol set may be periodically set to a known value by the transmitter to assist channel tracking. One symbol in every symbol set can even be set equal to a known symbol, as with high order OSDM, that only consumes 5 $1/N$ th of the channel capacity. Moreover, when one or more of the symbols in a square is known a-priori, the equations for the unknown symbols become over dimensioned and can be advantageously solved in a least-squares sense.

In the above derivation of an OSDM receiver it was assumed that all the orthogonal codes were carrying symbols to be decoded. If it is desired to omit decoding some symbols of a set and to decode a subset of n symbols of the set only, where $n < N$, then the size of the above matrices may be reduced from $N \times N$ to $N \times (n+1)$, the first n rows corresponding to symbols desired to be decoded and quantized, and the $(n+1)$ 'th row corresponding to an interfering waveform which is the sum of the remaining symbols not desired to be decoded, and moreover not therefore quantized when fed forward to subtract delayed ISI when decoding the next symbol. This represents the generalization of the 2×2 case used in introducing the invention, which, in its interference-cancellation mode, comprises Decision Feedback of previously decoded values some of which are quantized and some of which are not quantized, thereby achieving the subtraction of multiple interferers in a single step.

As has been shown in U.S. patent no. 5,937,015 to Dent and Bottomley, entitled 20 "Interference Mitigation by Joint Decoding of Overlapped Signals", error correction coding may be

employed to improve the reliability of already-decoded symbols that are used in decoding further symbols, with appropriate choice of interleaving pattern. The interleaving pattern determines the order in which bits from a receiver such as disclosed in this invention are fed to the error correction decoder. The preferred interleaving order is to select soft-bits from the just-decoded symbol set to feed to the error correction decoder followed by soft-bits that remain from previously decoded symbol sets. That allows the latter, now assumed to be more reliable bits, to flush through the bits from the just-decoded symbol thereby improving the reliability of bits from the just decoded symbol.

These will be used to flush through soft bits from the next-decoded symbol, and so on. The '015 patent is hereby incorporated by reference herein together with its parent, U.S. patent no. 5,673,291 entitled "Simultaneous Demodulation and Decoding of a Digitally Modulated Radio Signal using Known Symbols".

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The present invention has been described with respect to a block diagram for a receiver, illustratively in a mobile terminal or a base station. It will be understood that each block of the block diagram can be implemented by computer program instructions. These program instructions may be provided to a processor to produce a machine, such that the instructions which execute on the processor create means for implementing the functions specified in the blocks. The computer program instructions may be executed by a processor to cause a series of operational steps to be performed by the processor to produce a computer implemented process such that the instructions which execute on the processor provide steps for implementing the functions specified in the blocks. Accordingly, the illustrations support combinations of means for performing a specified

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function and combinations of steps for performing the specified functions. It will also be understood that each block and combination of blocks can be implemented by special purpose hardware-based systems which perform the specified functions or steps, or combinations of special purpose hardware and computer instructions.

5 A person skilled in the art may make numerous modifications or adaptations to
receivers and transmitters based on the above teachings and those of the incorporated disclosures,
while remaining within the scope and spirit of this invention as described by the attached claims.